

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Patent Application of

Peter J. Fricke et al.

Application No. 10/772,945

Filed: February 4, 2004

For: Memory Array with Two-Terminal  
Crosspoints Using Silicon-Rich  
Insulator

Group Art Unit: 2811

Examiner: Ori Nadav

Confirmation No.: 5316

AMENDMENT

Commissioner for Patents  
Mail Stop Amendment  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Commissioner:

In response to the Action under *Ex parte Quayle* dated June 14, 2010 in the above-identified patent application, please consider the following.

A Listing of Claims begins on Page 2 of this response.

Remarks begin on Page 11 of this response.